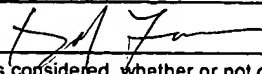


Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. <b>240428US2S</b>		SERIAL NO. <b>NEW APPLICATION</b>	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT <b>Toru TANZAWA</b>			
				FILING DATE <b>HEREWITH</b>		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
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	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
DL	AW	M. BAUER, et al. "A MULTILEVEL-CELL 32MB FLASH MEMORY," 1995 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, February 1995, pgs. 132-133.					
DL	AX	G. CAMPARDO, et al. "A 40MM <sup>2</sup> 3V 50MHZ 64MB 4-LEVEL CELL NOR-TYPE FLASH MEMORY," 2000 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, February 2000, pgs. 274-275.					
DL	AY	D. ELMHURST, et al. "A 1.8V 128MB 125 MHZ MULTI-LEVEL CELL FLASH MEMORY WITH FLEXIBLE READ WHILE WRITE," ISSCC 2003 Digest of Technical Papers, February 2003, pgs. 286-287.					
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner 					Date Considered <b>7/04</b>		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							